

Appl. No. 10/092,874



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/092,874
Filing Date March 5, 2002
Inventor Yoshiki Hishiro
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner Peralta, Ginette
Attorney's Docket No. MI22-1873
Title: Semiconductor Constructions; and Methods of Forming Semiconductor
 Constructions

RESPONSE TO MARCH 10, 2003 OFFICE ACTION

To: Box Non-Fee Amendment
 Commissioner for Patents
 Washington, D.C. 20231

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
 Wells St. John P.S.
 601 W. First Avenue, Suite 1300
 Spokane, WA 99201-3828

AMENDMENTS

Amendments to the Claims

Please cancel claims 1-18.

RECEIVED
MAR 10 2003
PATENT & TRADEMARK OFFICE

EV182658117